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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: James D. Gallia, et al.

Docket No: TI-32942

Serial No: 10/035,606

Conf. No: 3751

Examiner: Shouxiang Hu

Art Unit: 2811

Filed: 12/28/2001

For: METHOD AND SYSTEM FOR REDUCING CHARGE DAMAGE IN SILICON-ON-INSULATOR TECHNOLOGY

AMENDMENT UNDER 37 C.F.R. § 1.111

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Commissioner For Patents
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MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(a)

I hereby certify that the above correspondence is being deposited with the U.S. Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on 4-4-05.

Ann Trent
Ann Trent

Dear Sir:

Responsive to the Office Action mailed September 26, 2003, in connection with the above identified application, Applicants respectfully submit the following amendments and remarks.